

FIG. 1A is a schematic diagram of a system 100.

100

101

102

108

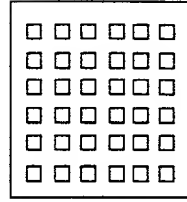
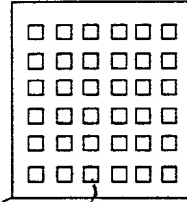
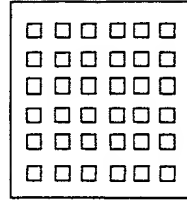
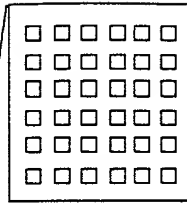


FIG. 1A

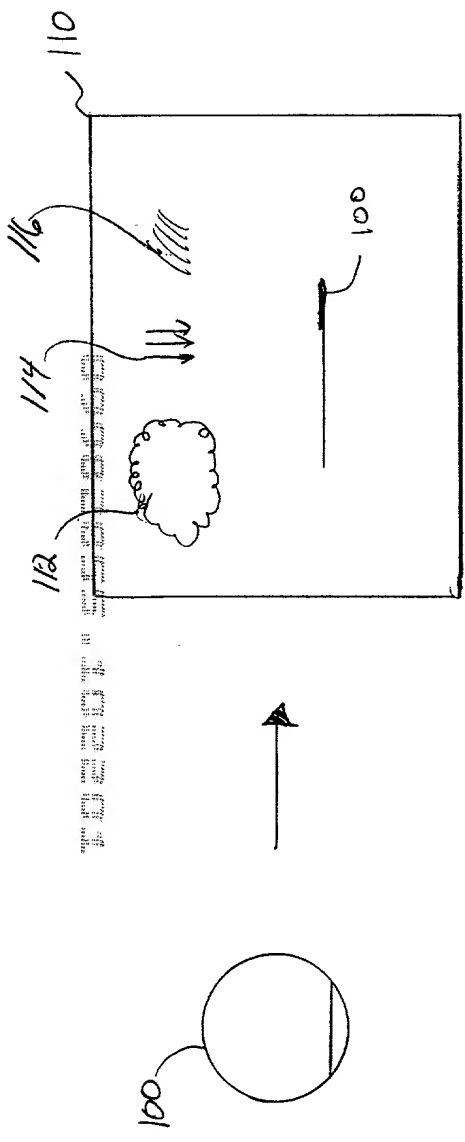


FIG. 1B



FIG. 1C

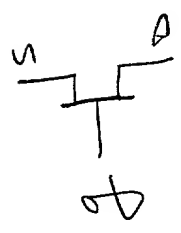


FIG. 1D

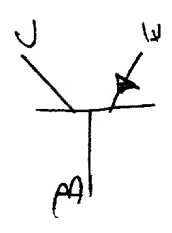


FIG. 1E

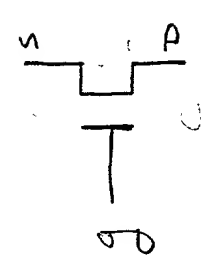
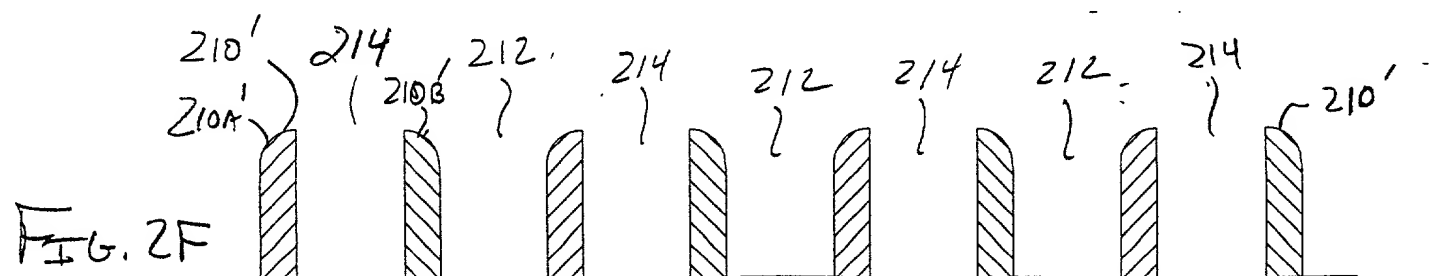
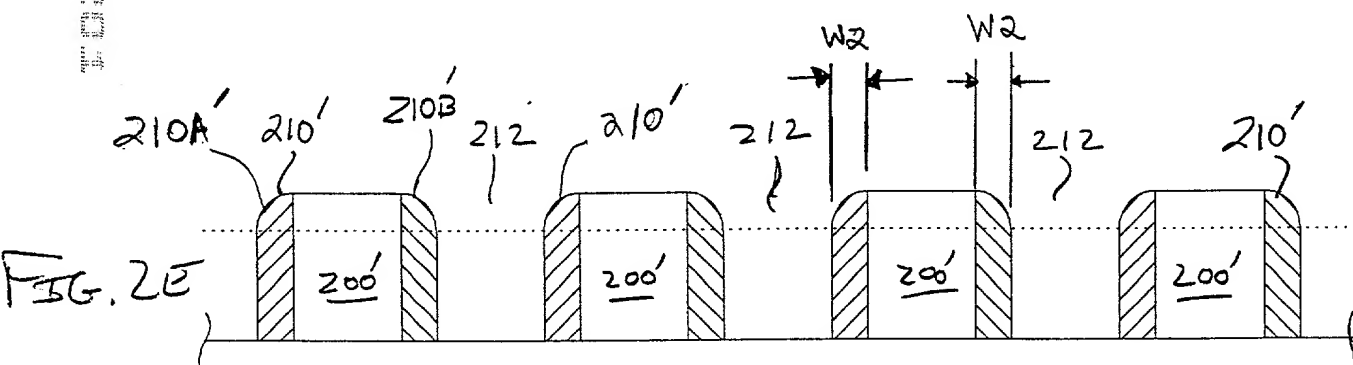
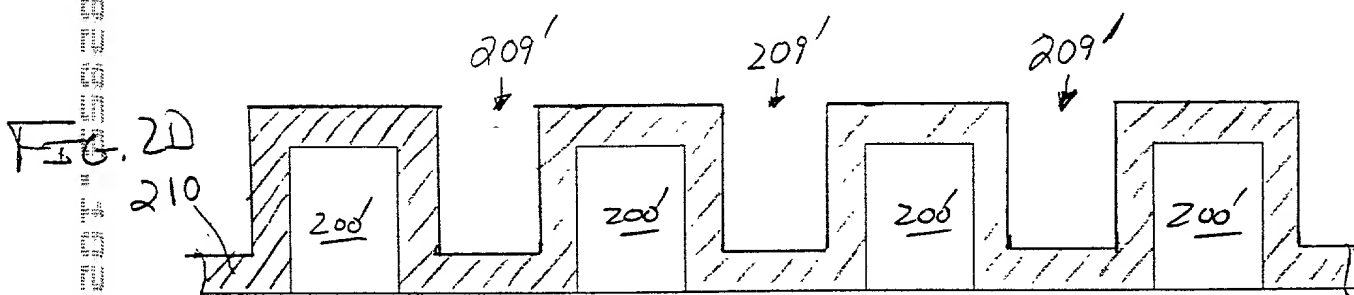
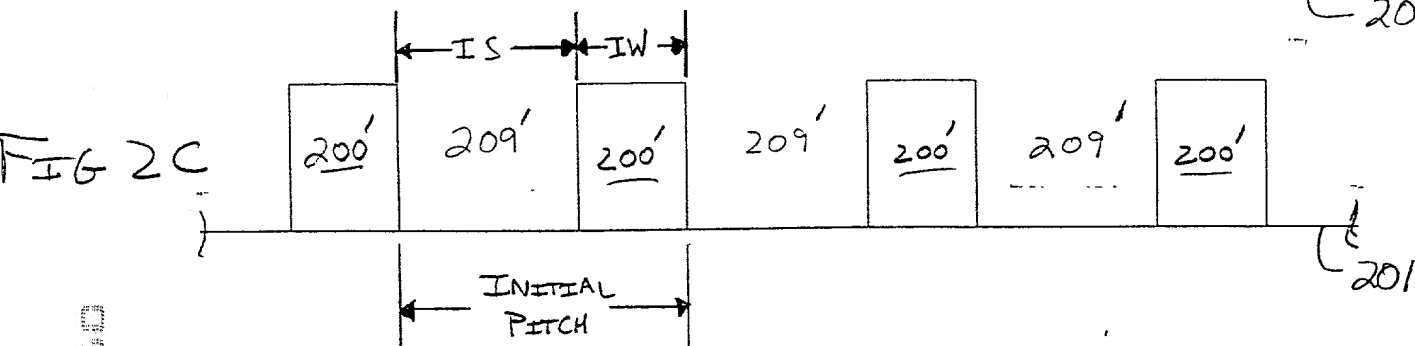
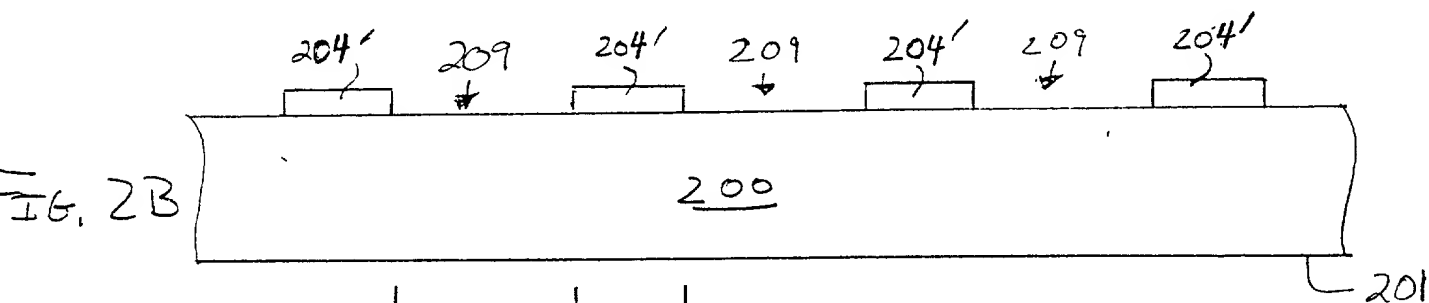
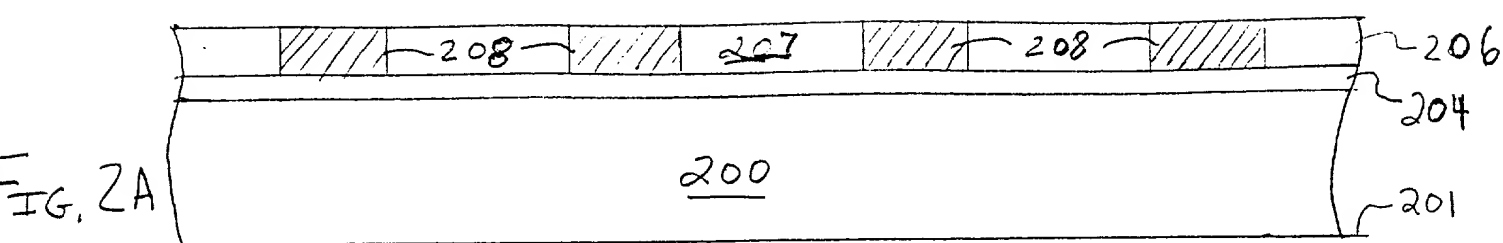


FIG. 1F



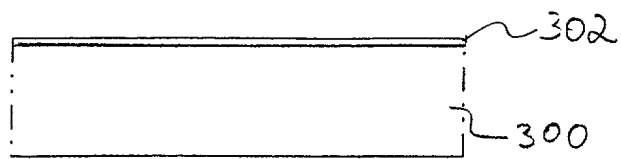


Fig. 3A

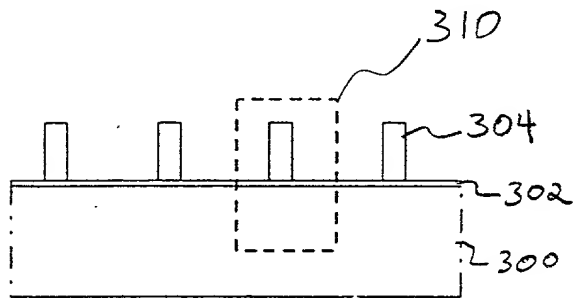


Fig. 3B

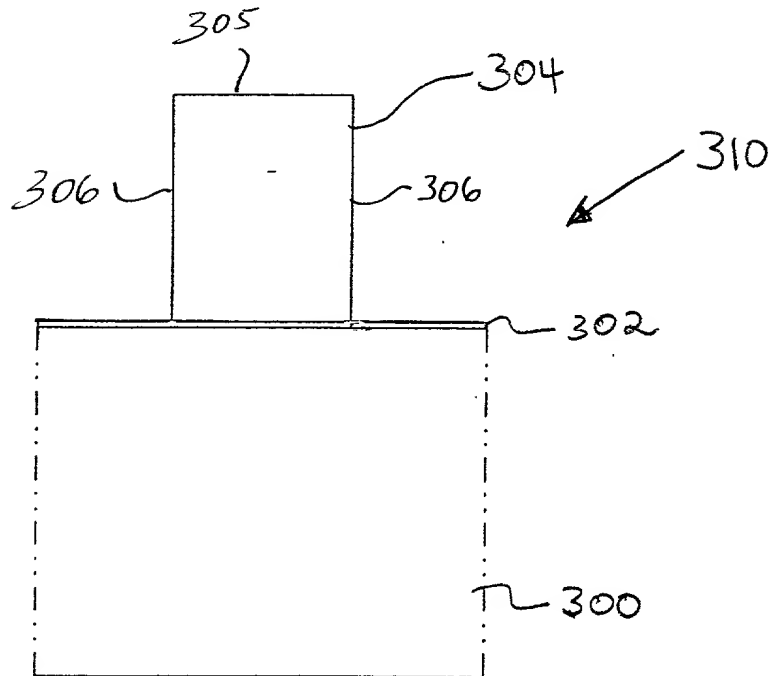


Fig. 3C

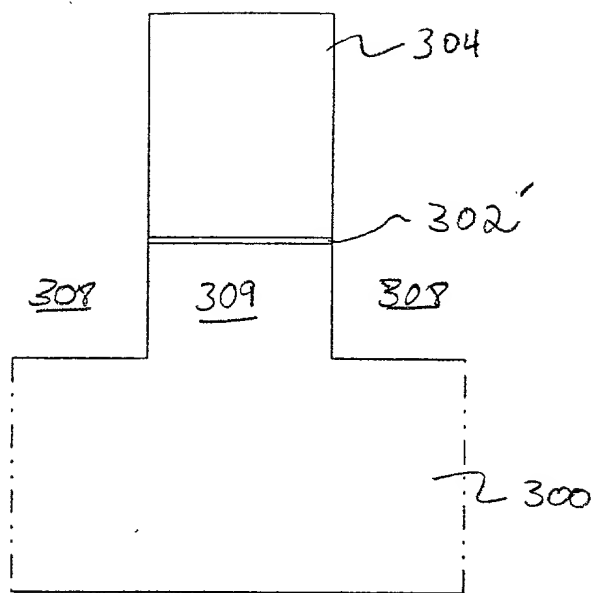


Fig. 3D

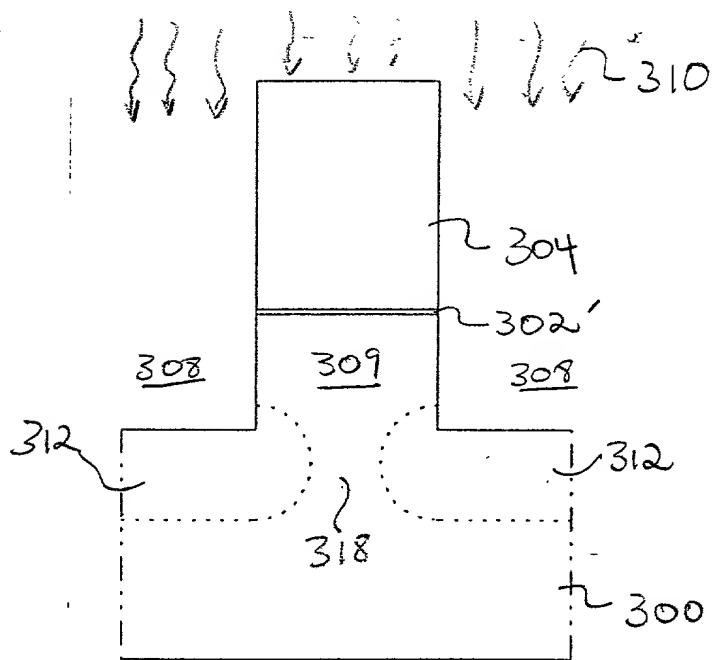


Fig. 3E

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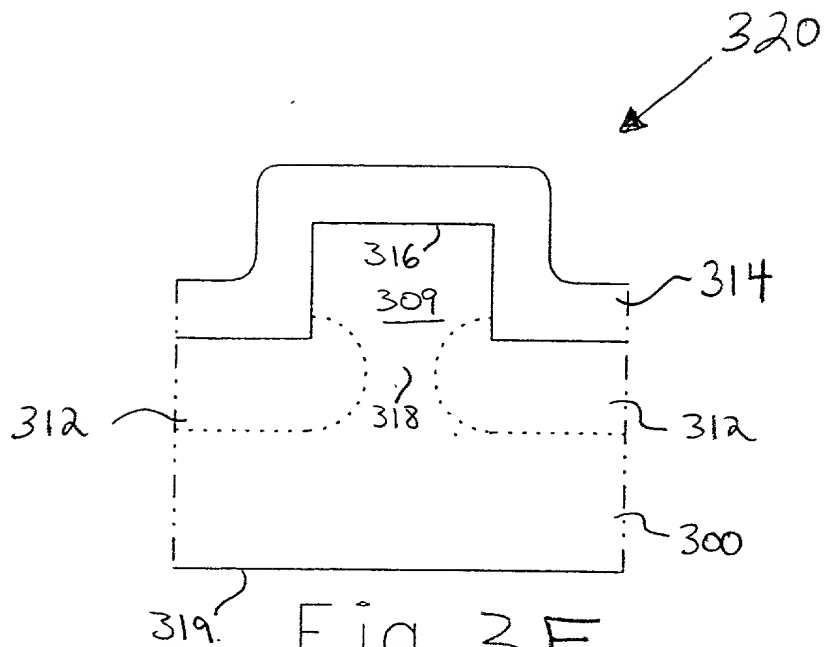


Fig. 3F

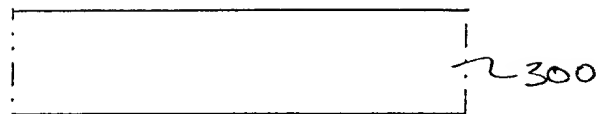


Fig. 4A

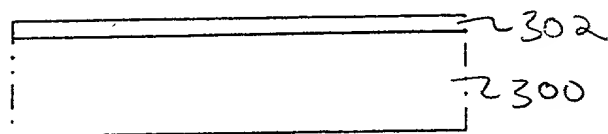


Fig. 4B

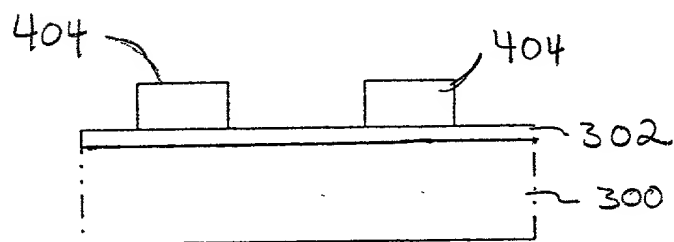


Fig. 4C

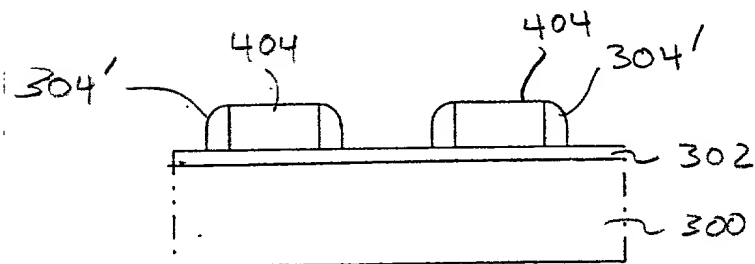


Fig. 4D

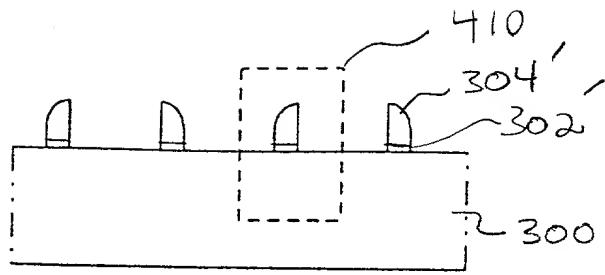


Fig. 4E

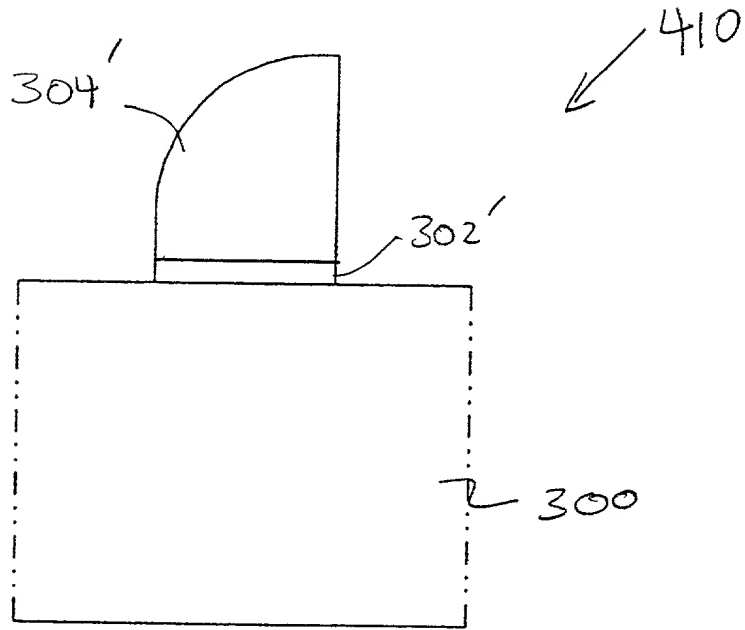


Fig. 4F

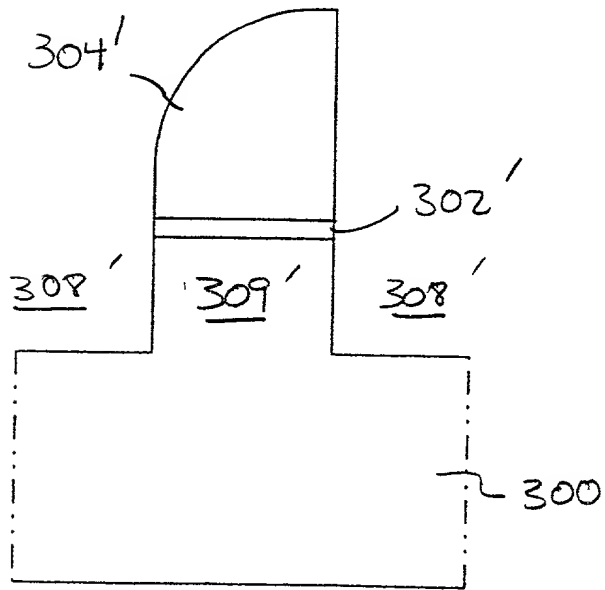


Fig. 4G



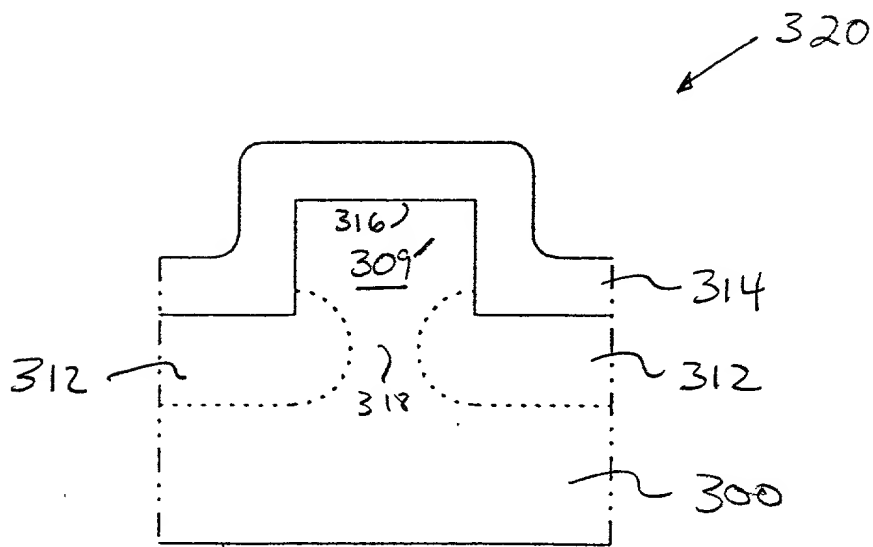
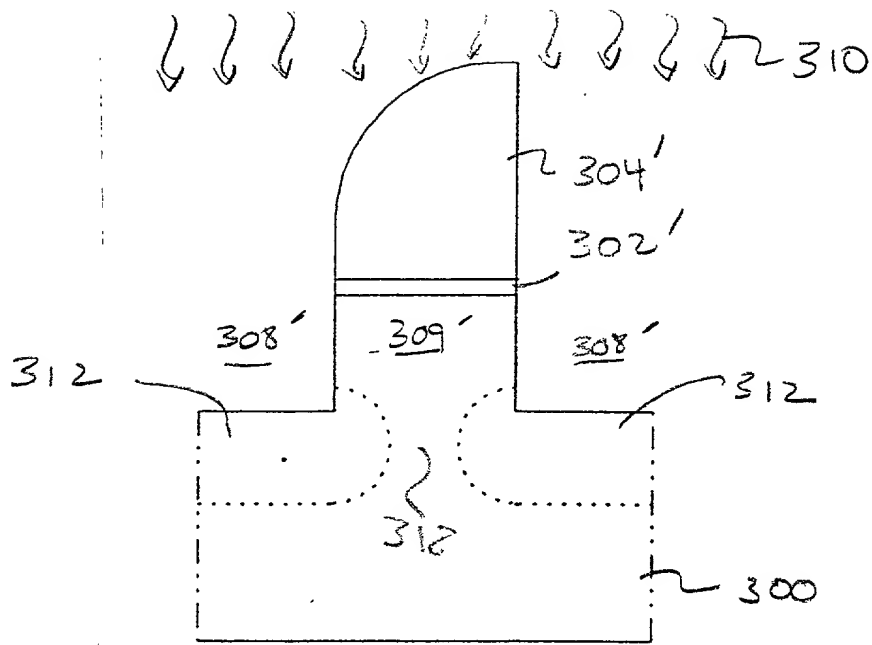


FIG. 5

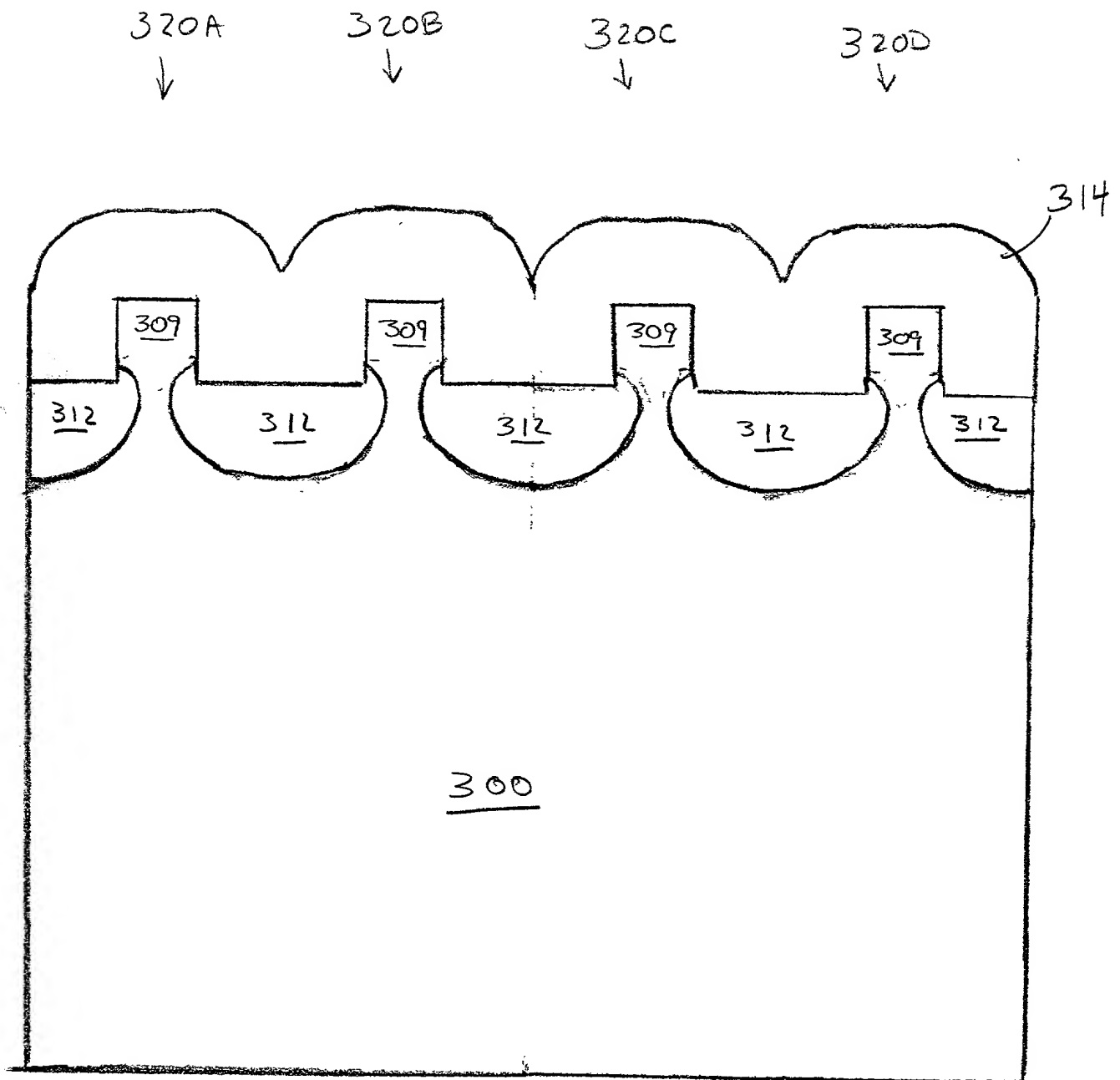


FIG. 5

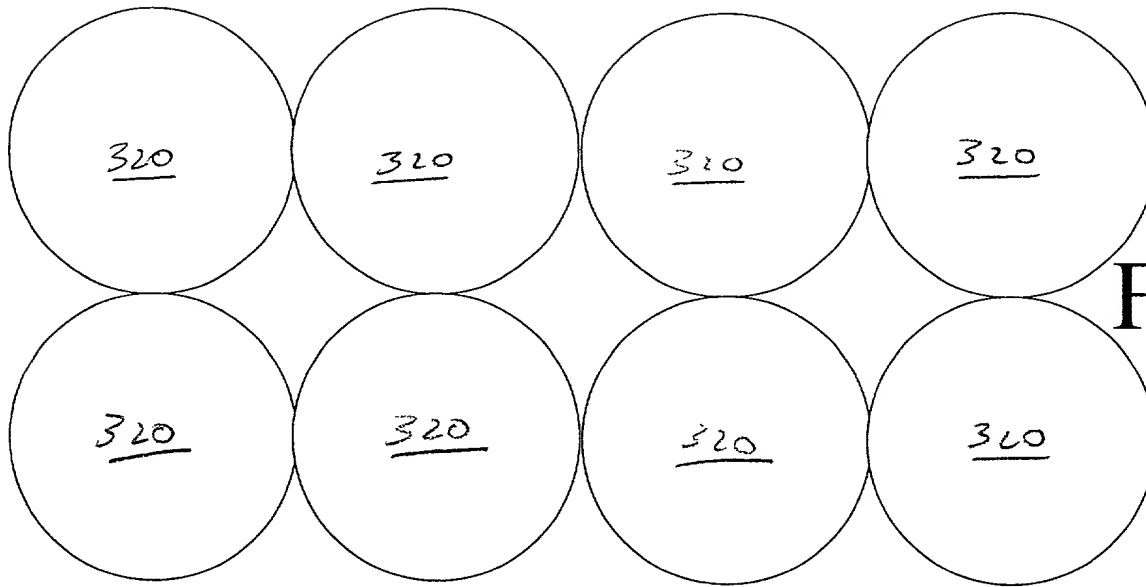


FIG. 6A

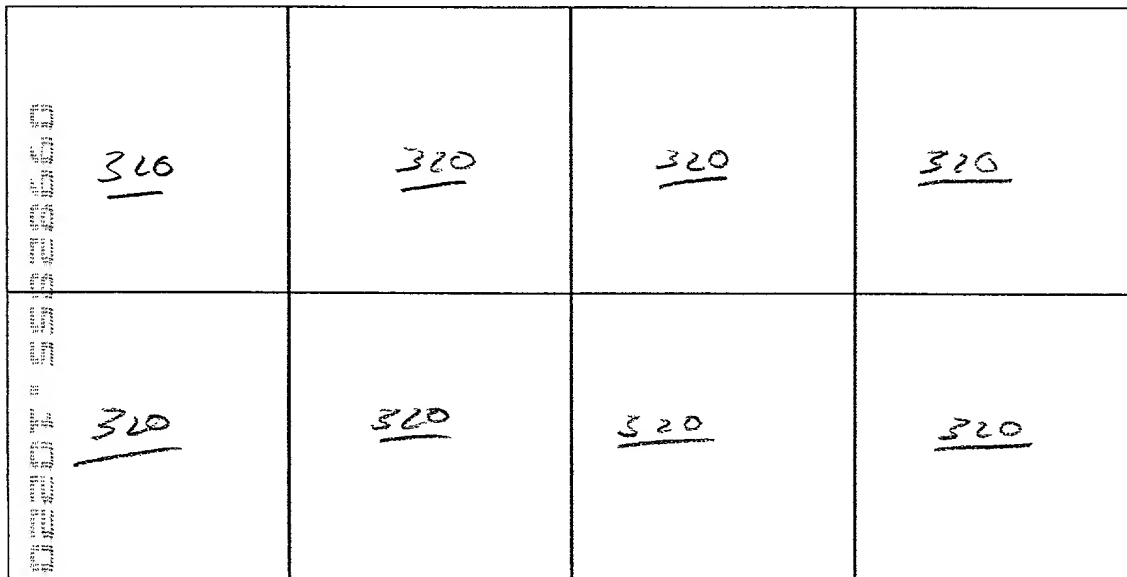


FIG. 6B

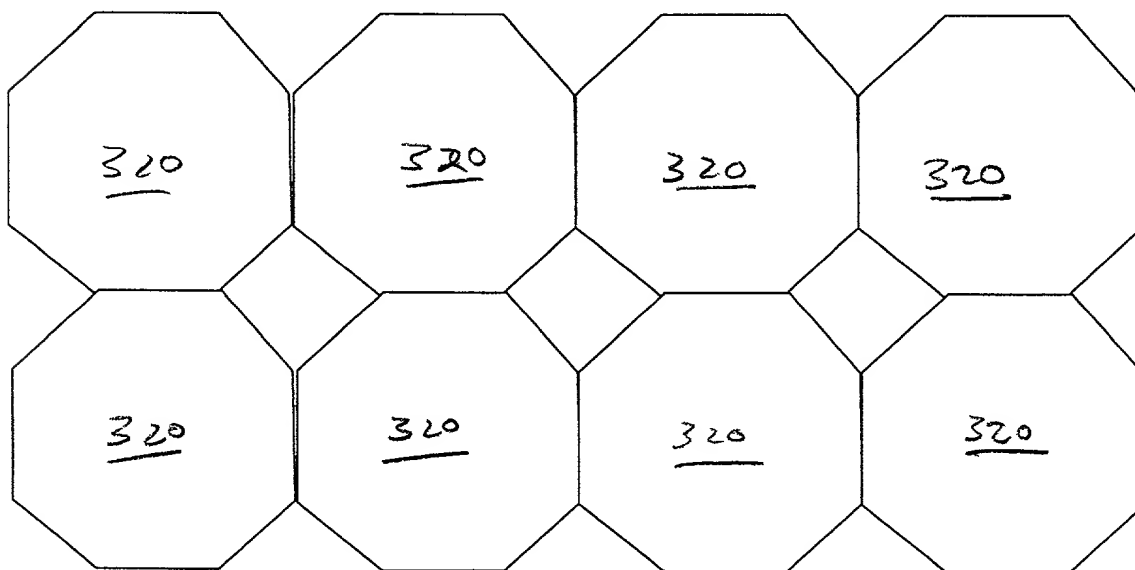


FIG. 6C

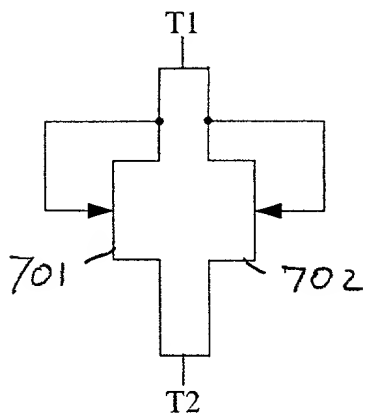


FIG. 7A

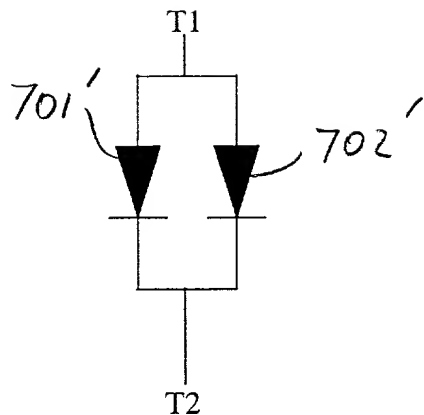


FIG. 7B

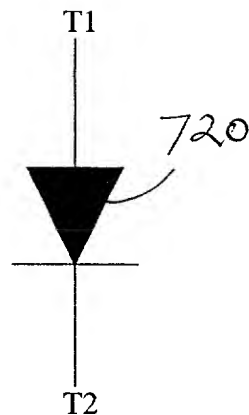


FIG. 7C

FIG. 7A is a schematic diagram of a cross-shaped circuit element. FIG. 7B is a schematic diagram of a circuit element with two downward-pointing triangles. FIG. 7C is a schematic diagram of a circuit element with a single downward-pointing triangle.

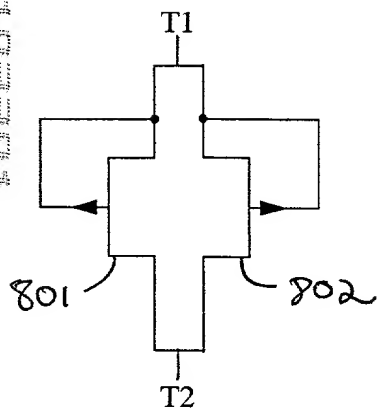


FIG. 8A

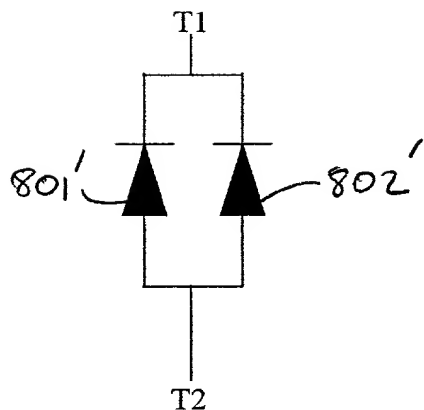


FIG. 8B

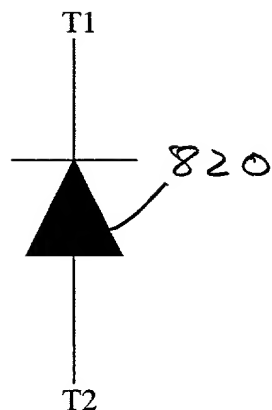


FIG. 8C

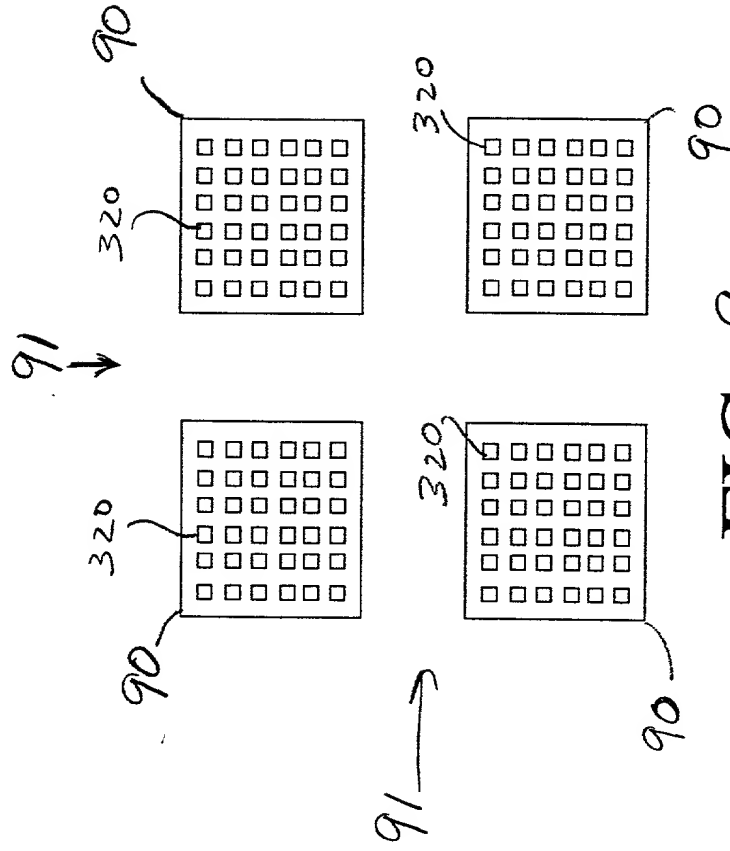


FIG. 9

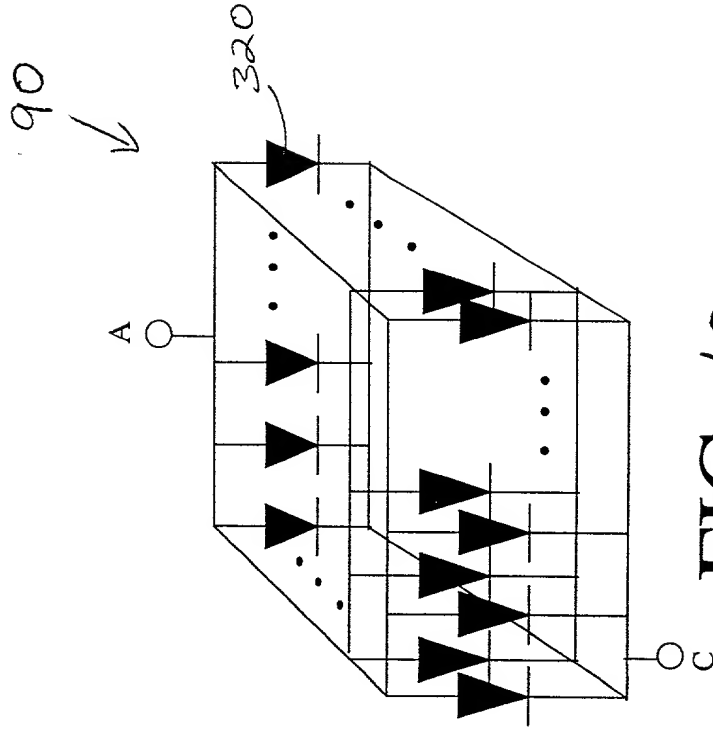


FIG. 10

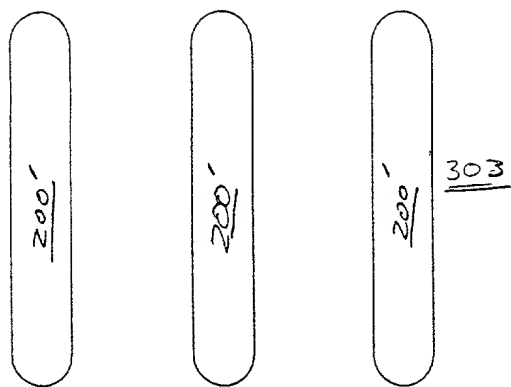


FIG. 11A

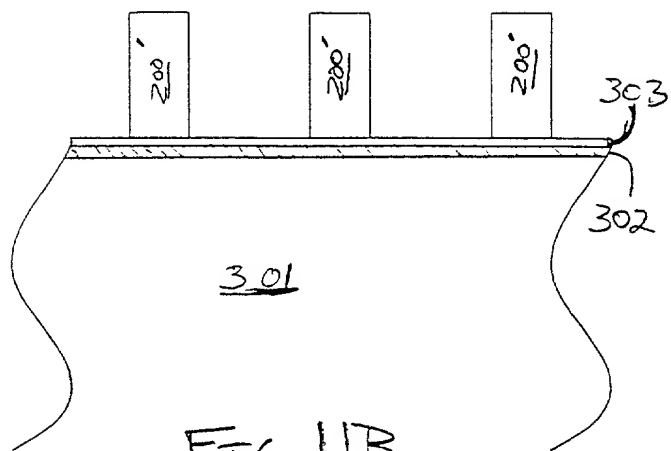


FIG. 11B

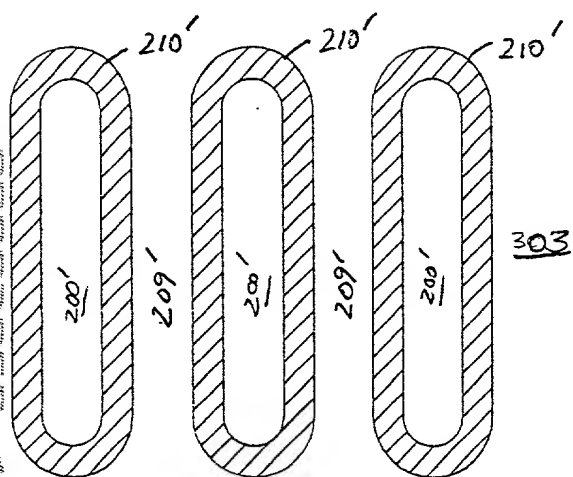


FIG. 12A

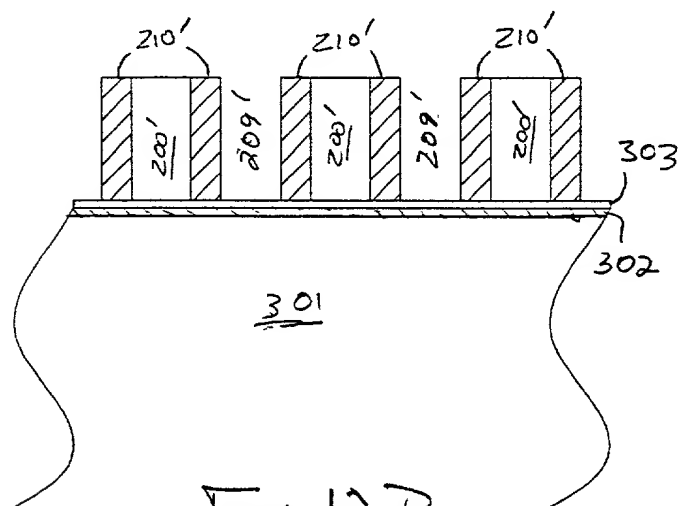


FIG. 12B

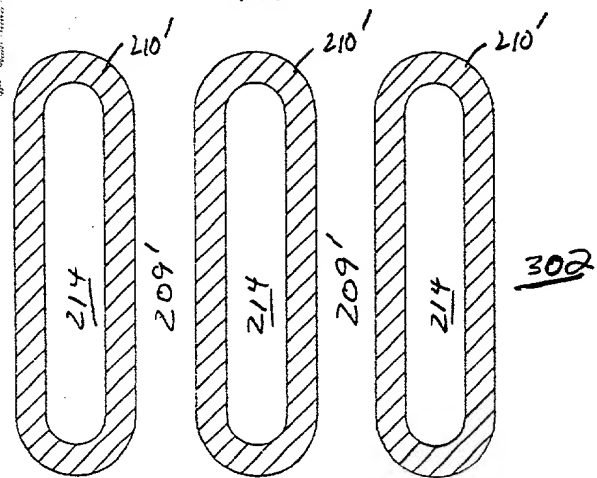


FIG. 13A

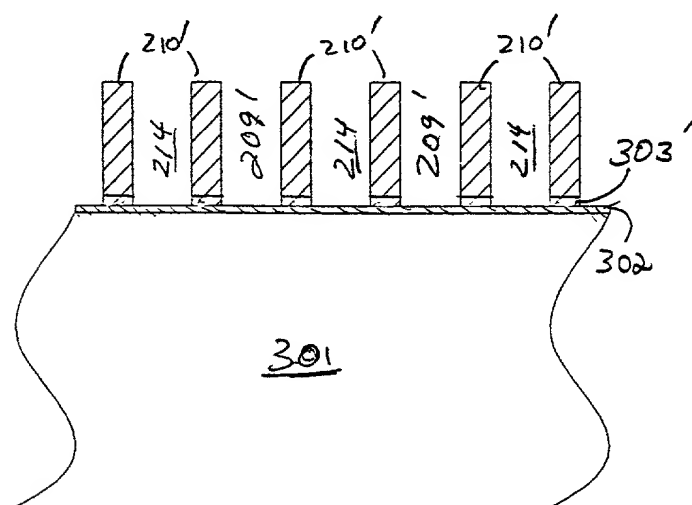


FIG. 13B

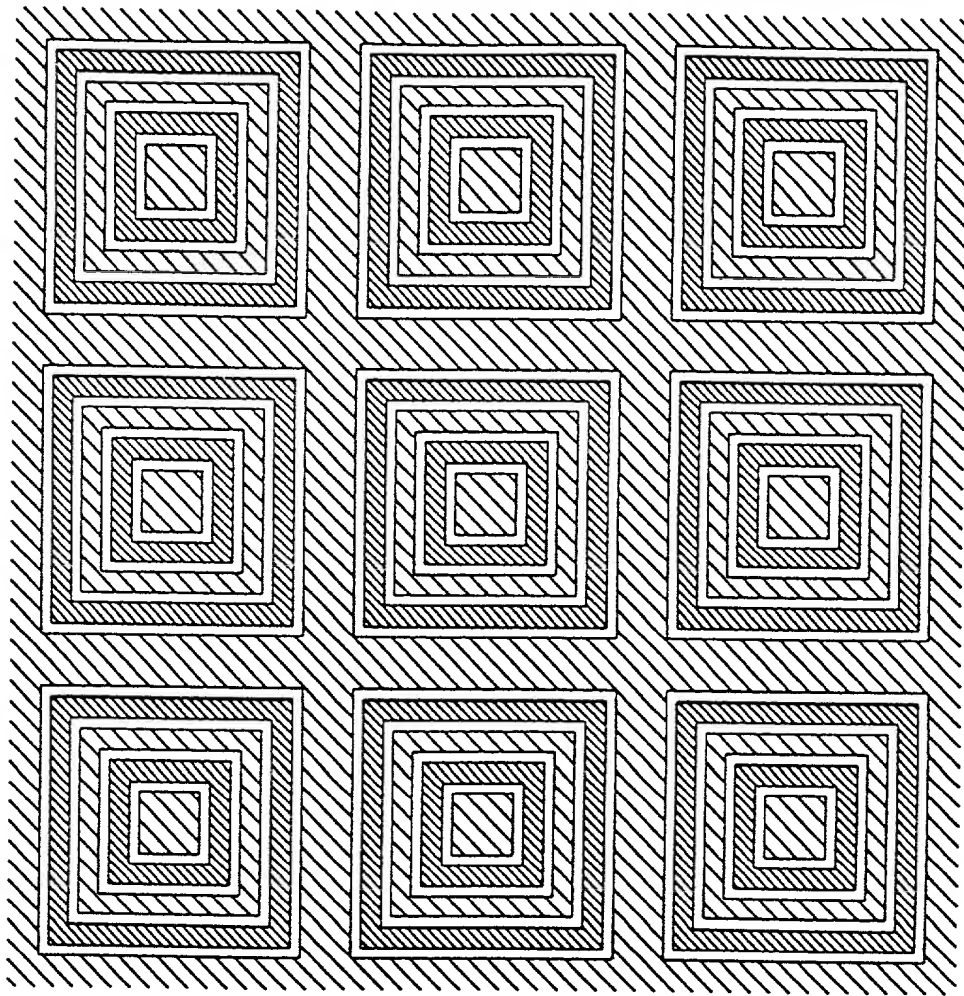


FIG. 16

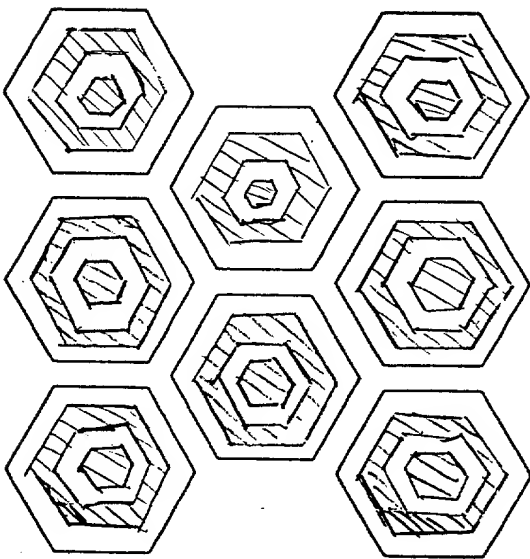


FIG. 14

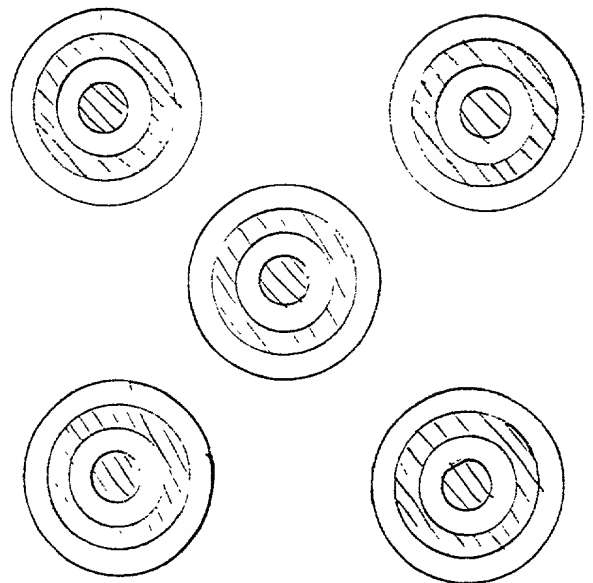


FIG. 15

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